North-Holland
Microprocessing and Microprogramming 26 (1989/1990) 361–362

Author Index to Volume 26

Acharya, A., S. Bhawmik, C.R.K. Prasad, and P. Palchaudhuri, KIDLAN: A Hardware Description Language 26 (1989/90) 1
Ali Soomro, Amjad., *see* Rahman, Mushfiqur 26 (1989/90) 129
Atiquzzaman, M., Hardware Implementation of a Parallel Noise Clearing Algorithm 26 (1989/90) 119

Bedi, J.S., *see* Chaudry, G.M. 26 (1989/90) 45
Bhawmik, S., *see* Acharya, A. 26 (1989/90) 1
Bhuyan, L.N., *see* Muppola, J.K. 26 (1989/90) 31
Burkhardt, W.H., Locality Aspects and Cache Memory Utility in Microcomputers 26 (1989/90) 51

Cabodi, G., P. Camurati, P. Prinetto, and M.S. Reorda, Expressing Logical and Temporal Conditions in Simulation Environments: TPDL 26 (1989/90) 241
Camurati, P., *see* Cabodi, G. 26 (1989/90) 241
Chan, K., *see* Rounce, P.A. 26 (1989/90) 139
Chaudhuri, P. Pal, *see* Das, Aloke K. 26 (1989/90) 195
Chaudry, G.M., and J.S. Bedi, On the Efficiency of Vector Computers with Private Cache Memories 26 (1989/90) 45
Cheang, S.M., *see* Leung, K.S. 26 (1989/90) 271
Constantinescu, C., Effect of Transient Faults on Gracefully Degrading Processor Arrays 26 (1989/90) 23
Crane, S., B. Tangney, and J. Moreau, Enforcing Determinism in a CSMA/CD Local Area Network 26 (1989/90) 205

Das, Aloke K., and P. Pal Chaudhuri, An Efficient On-Chip Deterministic Test Pattern Generation Scheme 26 (1989/90) 195
Dobnikar, A., V. Gustin, M. Trebar, D. Podbregar, and P. Ilija, Programming Support for IC Design in ULA Technology 26 (1989/90) 213

Elizabeth M. C. Hull, and Adib Zarea-Alibadi, Real-Time System Implementation – The Transputer and Occam Alternative 26 (1989/90) 77

Govindarajan, R., R. Kumar, D. Kumar and L.M. Patnaik, PROMIDS: A PROtotype Multl-ring Data flow System for Functional Programming Languages 26 (1989/90) 161
Gustin, V., *see* Dobnikar, A. 26 (1989/90) 213

Hurson, A.R., *see* Lee, B. 26 (1989/90) 253
Hurson, A.R., *see* Miller, L.L. 26 (1989/90) 289
Hurson, A.R., *see* Shirazi, B. 26 (1989/90) 97

Kumar, D., *see* Govindarajan, R. 26 (1989/90) 161
Kumar, R., *see* Govindarajan, R. 26 (1989/90) 161

Lee, B., and A.R. Hurson, Hybrid Structure: Data Structuring for Data Flow 26 (1989/90) 253
Lee, K.H., *see* Leung, K.S. 26 (1989/90) 271
Leung, K.S., K.H. Lee, and S.M. Cheang, Lists Processing for Artificial Intelligence Applications 26 (1989/90) 271
Li, San-li, *see* Li, Ya-min 26 (1989/90) 113
Li, Ya-min, and San-li Li, Optimizing Allocation of Multi-port Register File for Parallelism in RISC and Its Implementation 26 (1989/90) 113
Lua, K.T., Relative Performance Measurement of 80386, 80286 and 8088 Personal Computer Systems 26 (1989/90) 85

Mackay, S., *see* Rounce, P.A. 26 (1989/90) 139
Miller, L.L., and A.R. Hurson, Interpretation of Null Values in Database Machine Architecture 26 (1989/90) 289
Moreau, J., *see* Crane, S. 26 (1989/90) 205
Muppola, J.K., and L.N. Bhuyan, Arbiter Designs for Multiprocessor Interconnection Networks 26 (1989/90) 31

Nicoud, J.M., *see* Tyrell, A.M. 26 (1989/90) 175

Palchaudhuri, P., *see* Acharya, A. 26 (1989/90) 1
Patnaik, L.M., *see* Govindarajan, R. 26 (1989/90) 161
Podbregar, D., *see* Dobnikar, A. 26 (1989/90) 213
Prasad, C.R.K., *see* Acharya, A. 26 (1989/90) 1
Prinetto, P., *see* Cabodi, G. 26 (1989/90) 241

Rahman, Mushfiqur, and Amjad Ali Soomro, An Efficient Numerical Technique to Calculate Buffer Statistics 26 (1989/90) 129
Rajaraman V., *see* Siva Ram Murthy, C. 26 (1989/90) 63

Reorda, M.S., *see* Cabodi, G. 26 (1989/90) 241
Rounce, P.A., K. Chan, and S. Mackay, VLSI Architecture Research within the ESPRIT SPAN Project 26 (1989/90) 139

Shirazi, B., and A.R. Hurson, A Data Driven Multiprocessor – Its Architecture and Performance Evaluation 26 (1989/90) 97
Shirazi, Behrooz, and Wang Ming-fang, Heuristic Functions for Static Task Allocation 26 (1989/90) 187
Siebert, Bernd R.L., *see* Skákala, J. 26 (1989/90) 15
Siva Ram Murthy C., and V. Rajaraman, Taks Assignment in a Multiprocessor System 26 (1989/90) 63
Skákala, J., K. Weise, and Bernd R.L. Siebert, A Personal Multicomputer for Parallel Processing of Long-running Programs 26 (1989/90) 15

Tangney, B., *see* Crane, S. 26 (1989/90) 205
Trebar, M., *see* Dobnikar, A. 26 (1989/90) 213
Tyrell, A.M., and J.D. Nicoud, Scheduling and Parallel Operations on the Transputer 26 (1989/90) 175

Wang, Ming-fang, *see* Shirazi, Behrooz 26 (1989/90) 187
Weise, K., *see* Skákala, J. 26 (1989/90) 15

Zarea-Alibadi, A., *see* Hull, E.M.C. 26 (1989/90) 77

North-Holland
Microprocessing and Microprogramming 26 (1989/90) 363–364

Subject Index to Volume 26

Additive CA 26 (1989/90) 195
Advanced microprocessors 26 (1989/90) 51
Arbiter designs 26 (1989/90) 31
Assertion specification 26 (1989/90) 241
AT2 costing 26 (1989/90) 195

Block driven architecture 26 (1989/90) 97
Buffer overflow probabilities 26 (1989/90) 129
Buffered register file 26 (1989/90) 113

Cache 26 (1989/90) 45
Cache memory performance 26 (1989/90) 51
CAD 26 (1989/90) 213
Cellular automata (CA) 26 (1989/90) 195
Channel routing 26 (1989/90) 213
Communicating Sequential Processes 26 (1989/90) 77
Communication 26 (1989/90) 205
Computational availability 26 (1989/90) 23
Computer architecture 26 (1989/90) 97
Concurrency 26 (1989/90) 253
Concurrent simulation 26 (1989/90) 241
Conditioned command language 26 (1989/90) 241
Crossbar 26 (1989/90) 31
CSMA/CD 26 (1989/90) 205

Data driven architecture 26 (1989/90) 97
Data flow 26 (1989/90) 253
Data locality 26 (1989/90) 51
Data structures 26 (1989/90) 253
Data-driven evaluation 26 (1989/90) 161
Database machines 26 (1989/90) 289
Determinism 26 (1989/90) 205
Distributed processing 26 (1989/90) 187

Error correcting code 26 (1989/90) 129

Fault tolerance 26 (1989/90) 231
Fiber optic 26 (1989/90) 231
Fill-shrink-and-expand 26 (1989/90) 119
Functional programming 26 (1989/90) 161

Gracefully degrading computer systems 26 (1989/90) 23
Group CA rule 26 (1989/90) 195
Grouping 26 (1989/90) 213

Hardware design 26 (1989/90) 161
Hardware implementation 26 (1989/90) 119
HDL 26 (1989/90) 1
Heuristic algorithms 26 (1989/90) 187
Heuristics 26 (1989/90) 63

IC design 26 (1989/90) 213
Image filtering 26 (1989/90) 119
Image processing 26 (1989/90) 119
Interconnection networks 26 (1989/90) 31
Interprogram communication 26 (1989/90) 15

KIDLAN 26 (1989/90) 1

LANs 26 (1989/90) 205
Lisp 26 (1989/90) 271
Lisp machine 26 (1989/90) 271
List-structured memory 26 (1989/90) 139
Lists processing 26 (1989/90) 271
Local Area Network 26 (1989/90) 231
Long-running programs 26 (1989/90) 15

MAP 26 (1989/90) 205
Markov chain 26 (1989/90) 45
Markov models 26 (1989/90) 23
Maybe algebra 26 (1989/90) 289
Microcomputer systems 26 (1989/90) 85
Microprocessor cache memory 26 (1989/90) 51
Mixed mode stimulation 26 (1989/90) 241
Moment generating function 26 (1989/90) 129
Monte Carlo programs 26 (1989/90) 15
Multi-microprocessors 26 (1989/90) 161
Multi-port register file 26 (1989/90) 113
Multi-ring Manchester data flow model 26 (1989/90) 161
Multiple-bus 26 (1989/90) 31
Multiprocessing 26 (1989/90) 97
Multiprocessor systems 26 (1989/90) 63, 31

Noise removal 26 (1989/90) 119
Null values 26 (1989/90) 289

Occam 26 (1989/90) 77
On-chip deterministic testing 26 (1989/90) 195
Operating systems 26 (1989/90) 187
Overflow 26 (1989/90) 113

Parallel computer architecture 26 (1989/90) 139
Parallel processes 26 (1989/90) 175
Parallel processing 26 (1989/90) 15, 97, 161
Parallelism 26 (1989/90) 77, 113, 253

PC/XT/AT 8088, 80286, 80386 26 (1989/90) 85
Performability 26 (1989/90) 23
Performance evaluation 26 (1989/90) 97, 119
Personal computers 26 (1989/90) 15, 85
Personal multicomputer system 26 (1989/90) 15
Placement 26 (1989/90) 213
Processor arrays 26 (1989/90) 23
Program locality 26 (1989/90) 51
Program statistics 26 (1989/90) 51

Queue 26 (1989/90) 129

Real-time 26 (1989/90) 77
Relational data-bases 26 (1989/90) 289
Reliability 26 (1989/90) 23, 31
Response time 26 (1989/90) 205
RISC 26 (1989/90) 113
Routing 26 (1989/90) 213

Scheduling 26 (1989/90) 175
Side-effect free operations 26 (1989/90) 253
Simulation 26 (1989/90) 253
Single board microcomputer 26 (1989/90) 15
Static scheduling 26 (1989/90) 187
Survivable networks 26 (1989/90) 231

Task assignment 26 (1989/90) 63
Task graph 26 (1989/90) 63
Temporal domain 26 (1989/90) 241
Time and space analysis 26 (1989/90) 253
Time model 26 (1989/90) 241
Token bus 26 (1989/90) 205
Token-passing ring 26 (1989/90) 231
Transient faults 26 (1989/90) 23
Transition matrix 26 (1989/90) 45
Transputer 26 (1989/90) 77, 175
2-D convolution 26 (1989/90) 119

ULA 26 (1989/90) 213
Underflow 26 (1989/90) 113

Vector computers 26 (1989/90) 45
VLSI design 26 (1989/90) 97

Wafer-scale integration 26 (1989/90) 97